

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Alexander J. Berger et al.  
Assignee: Tru-Si Technologies, Inc.  
Title: ALIGNMENT OF SEMICONDUCTOR WAFERS AND OTHER ARTICLES  
Application No.: 09/905,218 Filing Date: July 13, 2001  
Confirmation No.: 4479  
Examiner: Janice Lee Krizek Group Art Unit: 3682  
Docket No.: M-11882 US

San Jose, California  
March 11, 2003

COMMISSIONER FOR PATENTS  
Washington, D. C. 20231

**AMENDMENT**

Dear Sir:

In reply to the Office Action dated January 14, 2003, please amend the above-identified application as follows:

**RECEIVED**  
MAR 24 2003  
**GROUP 3600**